A Cost and Scalability Comparison of the Dragonfly versus the Fat Tree

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#### AGENRA

- O Motivation
- O Method
- The fat-tree topology
- O The dragonfly topology
- O Blocking, cost, and scalability

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O Conclusion

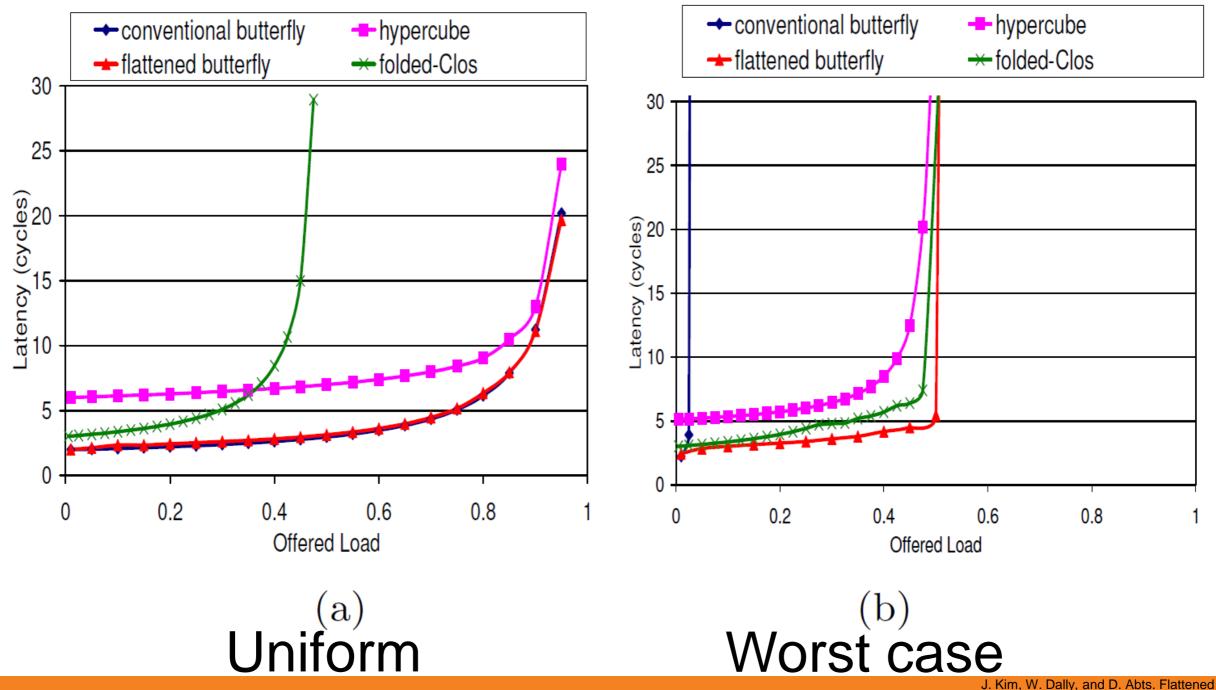
### MOTIVATION

The fat-tree is the dominating topology for InfiniBand networks, but the proposed dragonfly topology has been suggested as an alternative. In that context we would like to answer the following questions:

- O Is the dragonfly a viable alternative to the fat-tree?
- O How do they compare in the fundamental properties blocking, cost and scalability?
- O What about traffic patterns?
- A comparison on equal terms with regards to CBB.
- O When should you choose which?

# Why the dragonfly

- The range of traffic patterns considered is important
- Uniform/random versus well-defined/shift/MPI collectives
- Where is the crossing point for costs/performance for the dragonfly and fat tree?



[ simula . research laboratory ]

J. Kim, W. Dally, and D. Abts. Flattened buttery: a cost-ecient topology for high-radix networks. In Proceedings of the 34th annual international symposium on Computer architecture, pages 126-137. ACM, 2007.

#### APPRAACH

We aim to establish the lower and upper performance bound for the (dragonfly) topology independent of any routing algorithm.

The study consists of three parts:

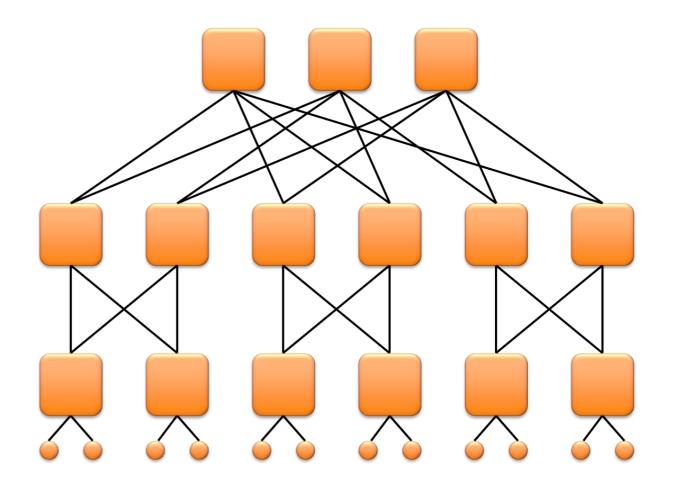
 Worst case analysis for the CBB ratio of the dragonfly and the fat-tree, giving a lower bound on performance.

 Permutation traffic analysis using linear programming, giving an upper bound on performance.

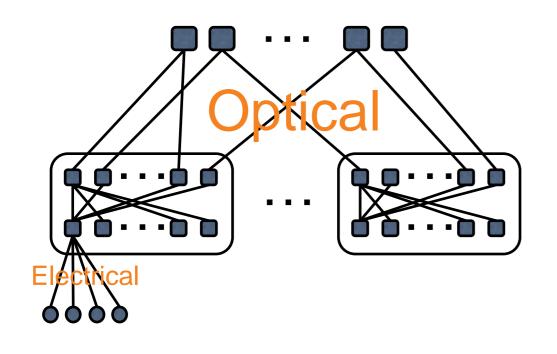
 Cost-scalability analysis by generating all possible topology sizes and apply a cost model to evaluate cost-efficiency.

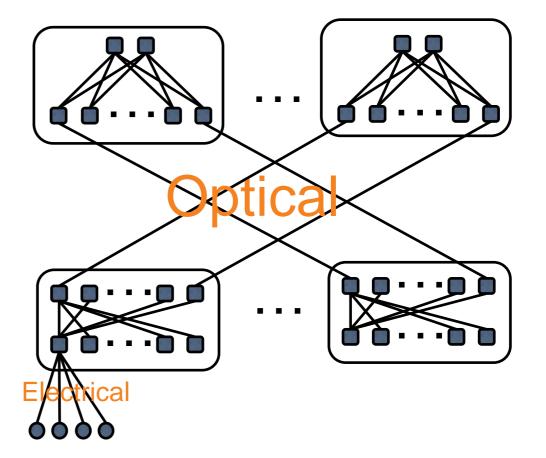
### THE EAT-TREE

- Most common topology for InfiniBand based computers
- Can be routed deadlock free without additional resources such as virtual lanes
- Fault-tolerant through its path diversity
- O Full bisection bandwidth for arbitrary permutations
- O Scalable, also with respect to cost
- O Performance suffers due to static routing, but adaptivity is supported



# THE EAT-TREE IMPLEMENTER





3 tiers

4 tiers

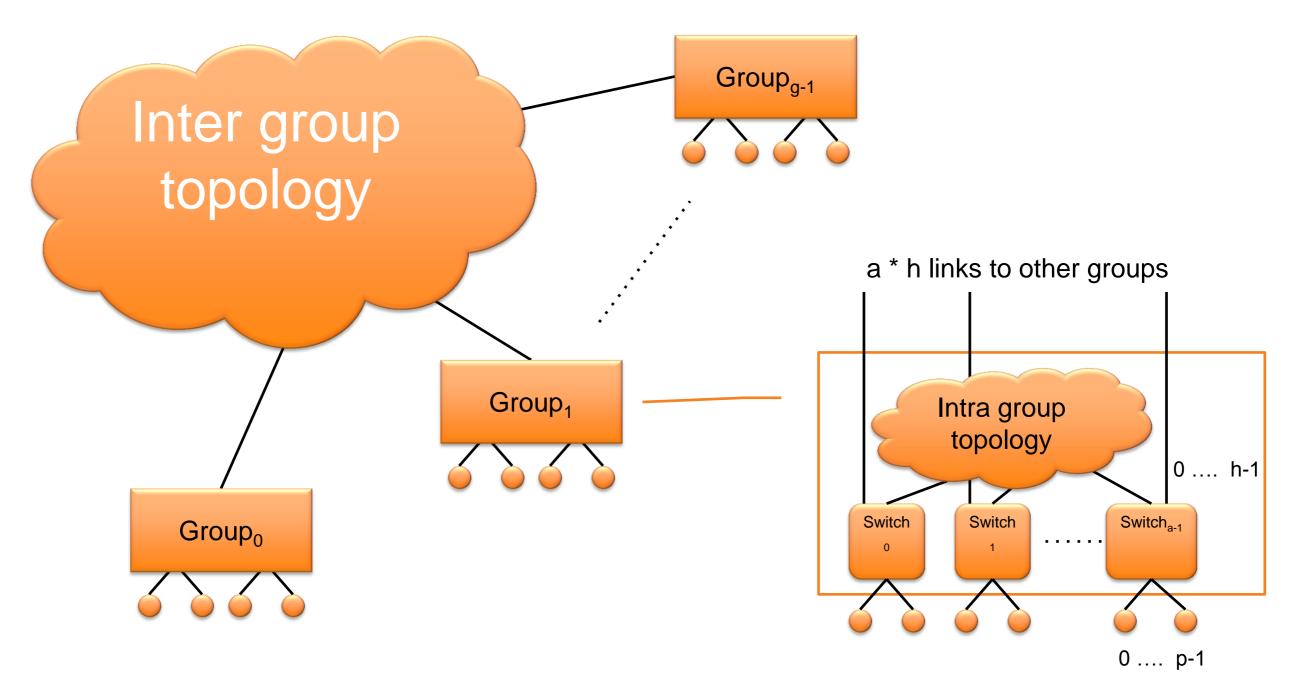
### THE BREESSELY

- O Recently proposed by John Kim et al. in [1].
- O Caused discussion in the HPC community about is suitability for IB and as an exascale topology.
- O The dragonfly is a hierarchical topology with the following properties:
  - O Several groups are connected together using all to all links, i.e. each group has at least one link directly to each other group.
    - The topology inside each group can be any topology. The recommendation in [1] is the flattened butterfly.
  - O Focus on reducing the number of long links and network diameter.
  - O Requires non-minimal global adaptive routing and advanced congestion look ahead for efficient operation.
  - O CBB ratio = 2 for its standard implementation

[1] John Kim et al. "*Technology-Driven, Highly-Scalable Dragonfly Topology*" in proceedings of the 35th

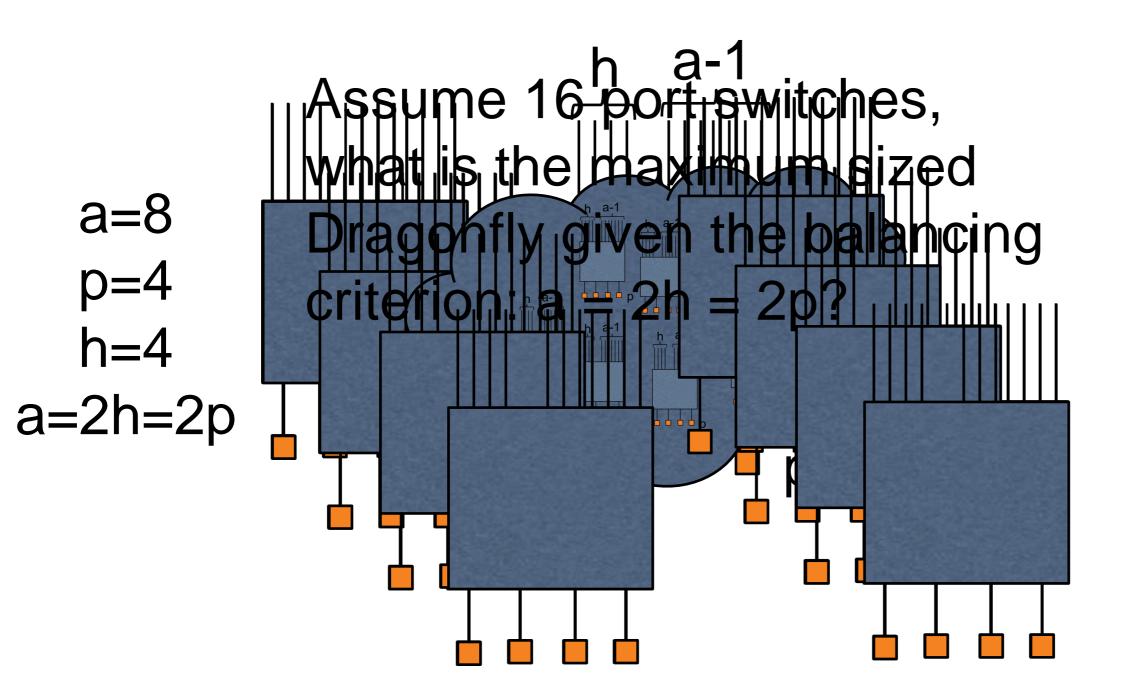
International Symposium on Computer Architecture, 2008.

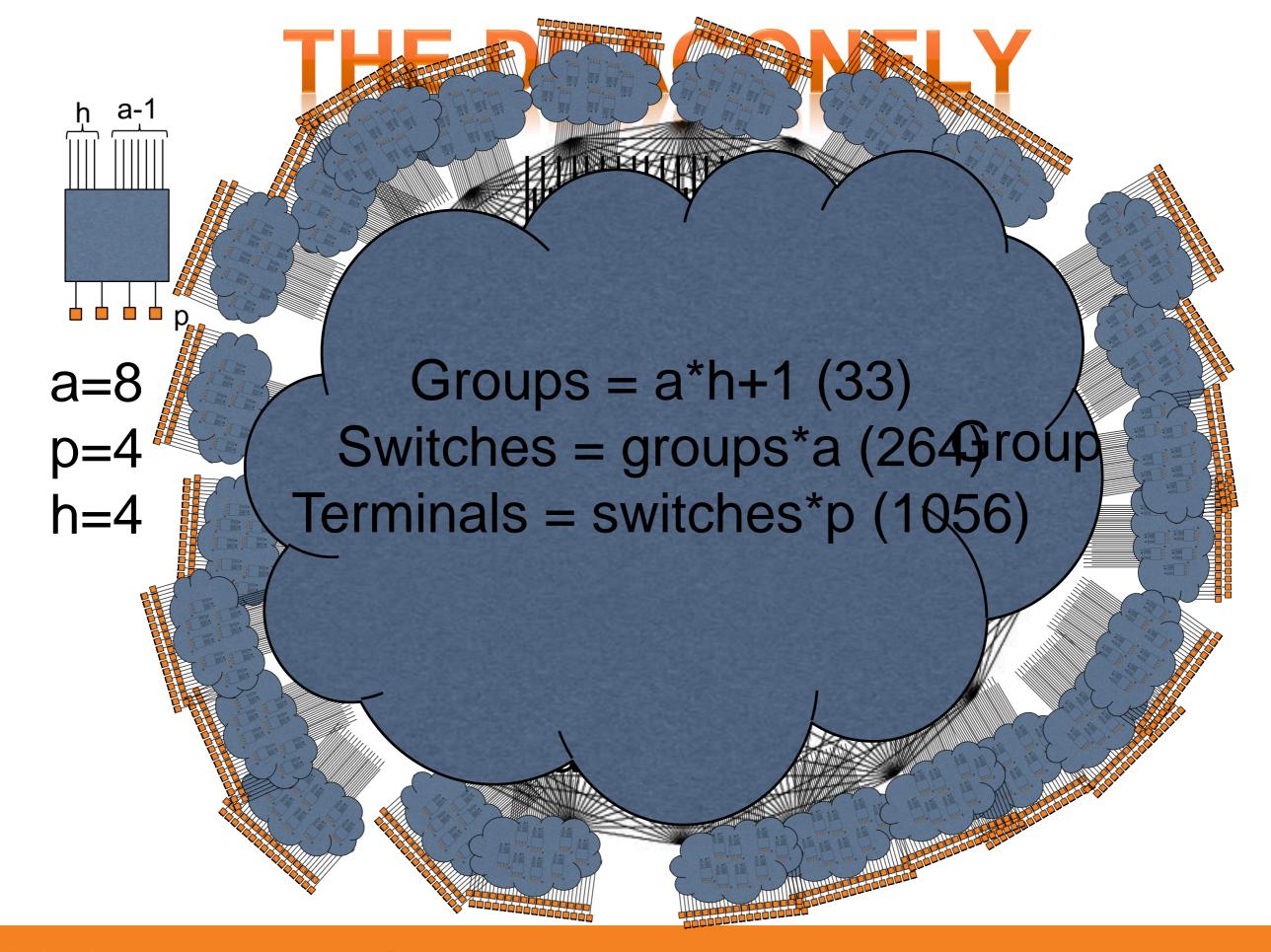
### THE REAGENELY



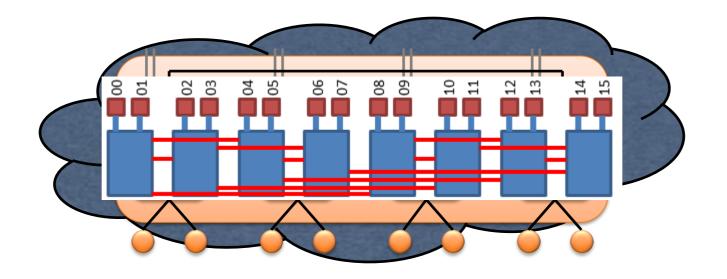
The recommendation in [1] is to keep:  $a^{3}2p^{3}2h$ 

#### THE REAGENELY



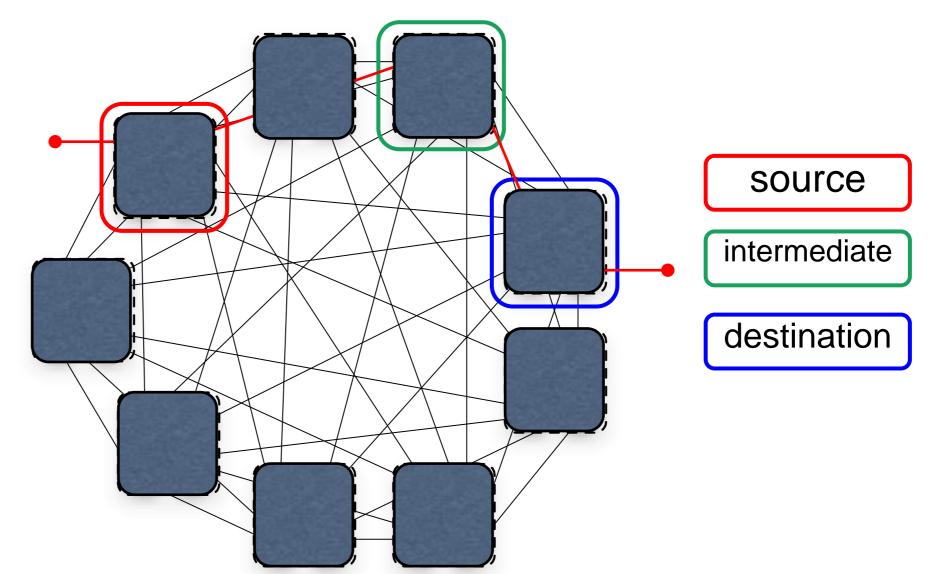


### **EBELY GROUP**



- A single flattened butterfly group with 8 terminals and 8 external connections.
- Fully connected, but requires also non-minimal adaptive routing for path diversity and load balancing.
- 2D flattened butterfly requires more internal routing.

### NON-MINIMAL HOPS



Dragonfly with a=4, p=2, and h=2. Can make non-minimal hops in the source (s), intermediate (i), and destination (d) group.



Global CBB ratio

$$CBB_{global} = \frac{2ap}{ah+1}$$

Local CBB ratio

$$CBB_{local}$$

$$= 2 * \frac{c(s) * p + u * h * \min(CBB_{global}, 1.0)}{\sqrt[n']{a}}$$

$$u = \min(1.0, c(i) + c(d))$$

### **CBB EQUATIONS**

The worst case occurs when the probability for making a non-minimal hop is one in all three groups : c(s) = 1, c(i) = 1, c(d) = 1

$$CBB_{local}$$

$$= 2 * \frac{1 * p + 1 * h * \min(CBB_{global}, 1.0)}{\sqrt[n']{a}}$$
For a standard dragonfly
$$CBB_{global} = \frac{2ap}{ah+1} \sim 2(1.98)$$

$$CBB_{local} = 2 * \frac{1 * p + 1 * h * 1}{a} = \frac{2(p+h)}{a} = 2$$



Using an LP solver to optimally place the paths for 10 000 permutations yields

$$c(s) = 0.74, c(i) = 0.15, c(d) = 0.74$$

for random permutations and

$$c(s) = 0.56, c(i) = 0.48, c(d) = 0.58$$

for group external permutations, i.e. all destinations are are outside the group.



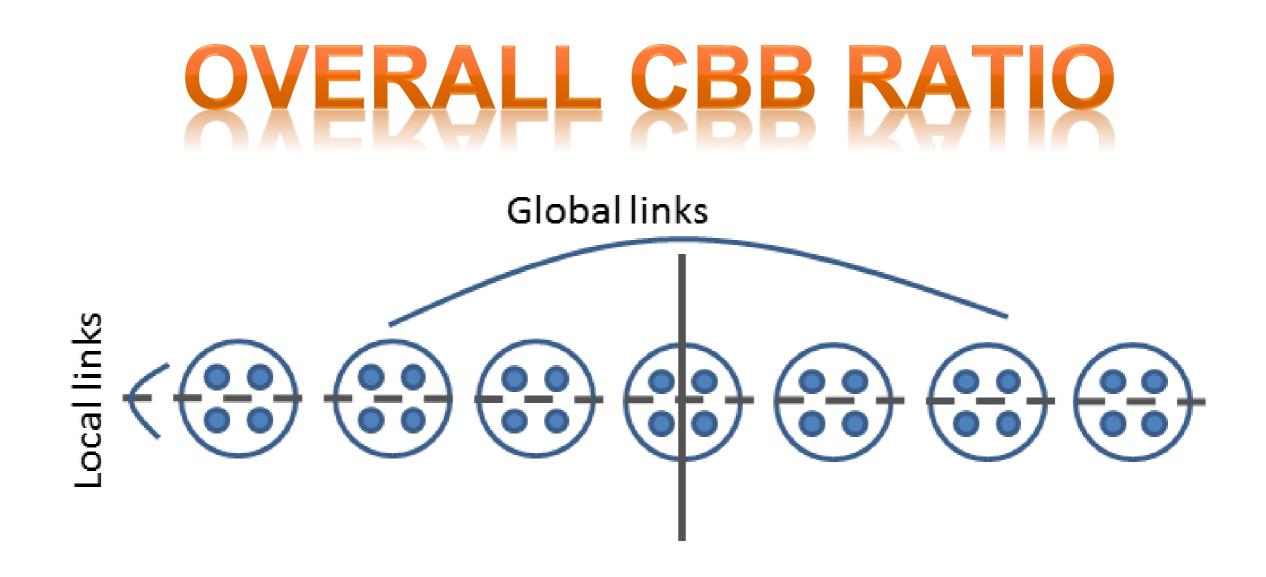
Example for the uniform traffic case:

c(s) = 0.74, c(i) = 0.15, c(d) = 0.74

For a standard dragonfly  

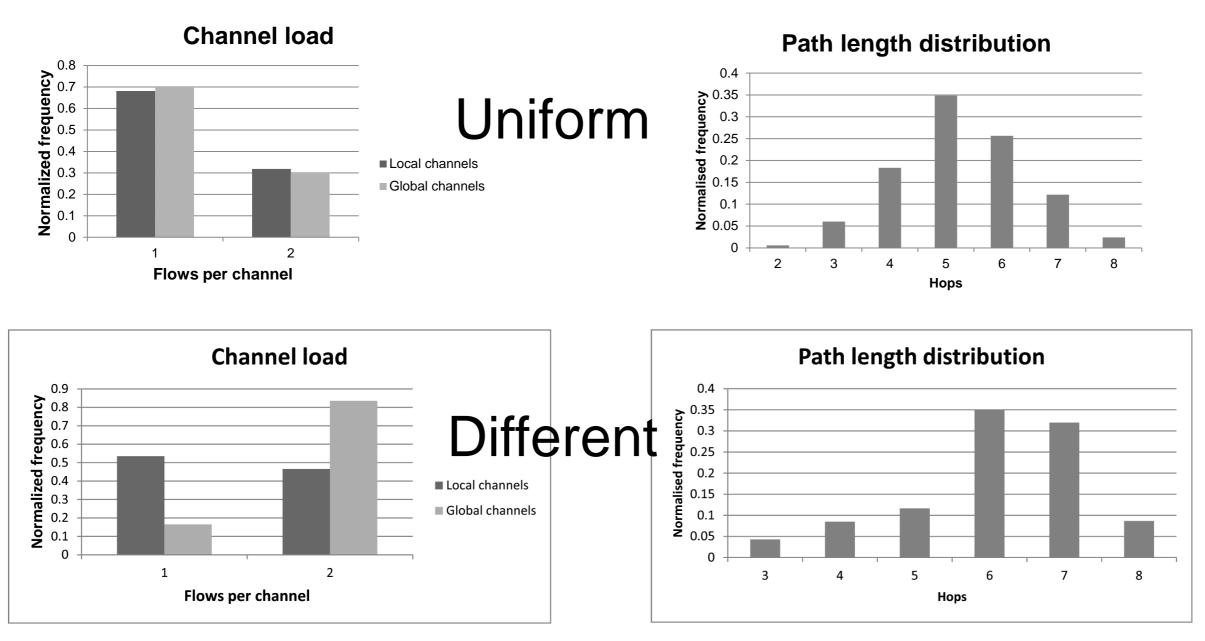
$$CBB_{global} = \frac{2ap}{ah+1} \sim 2(1.98)$$

$$CBB_{local} = 2 * \frac{0.74 * p + 0.89 * h * 1}{\sqrt[n']{a}}$$
$$= \frac{2 (0.74p + 0.89h)}{a} = 1.63$$



#### $CBB = \max(CBB_{local}, CBB_{global})$

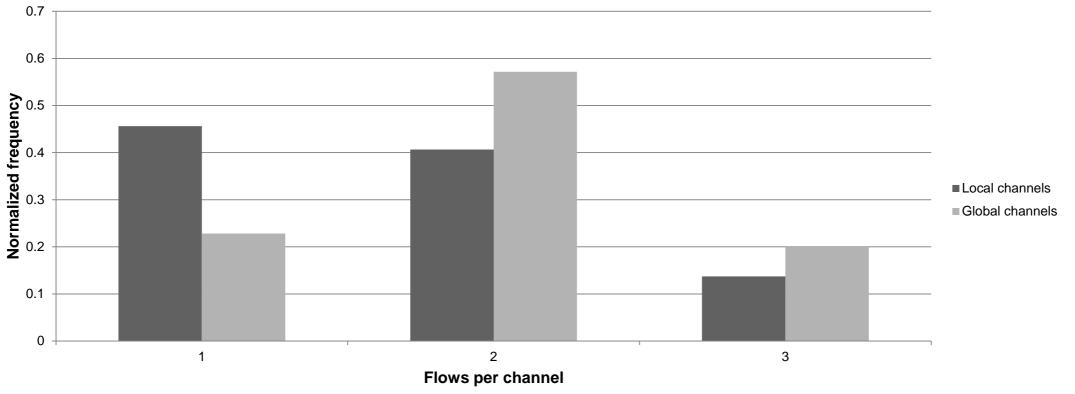




Statistics for 10 000 uniform (above) and random different group (below) permutations on a dragonfly with flattened butterfly groups.

#### CHANNEL USAGE WITH MINIMAL LOCAL ROUTING

Channel load



Minimal local routing leads to increased maximum channel load on the local channels (the increased global channel load is a function of the LP constraint)

# SCALABILITY AND COST

We compare the following four topologies:

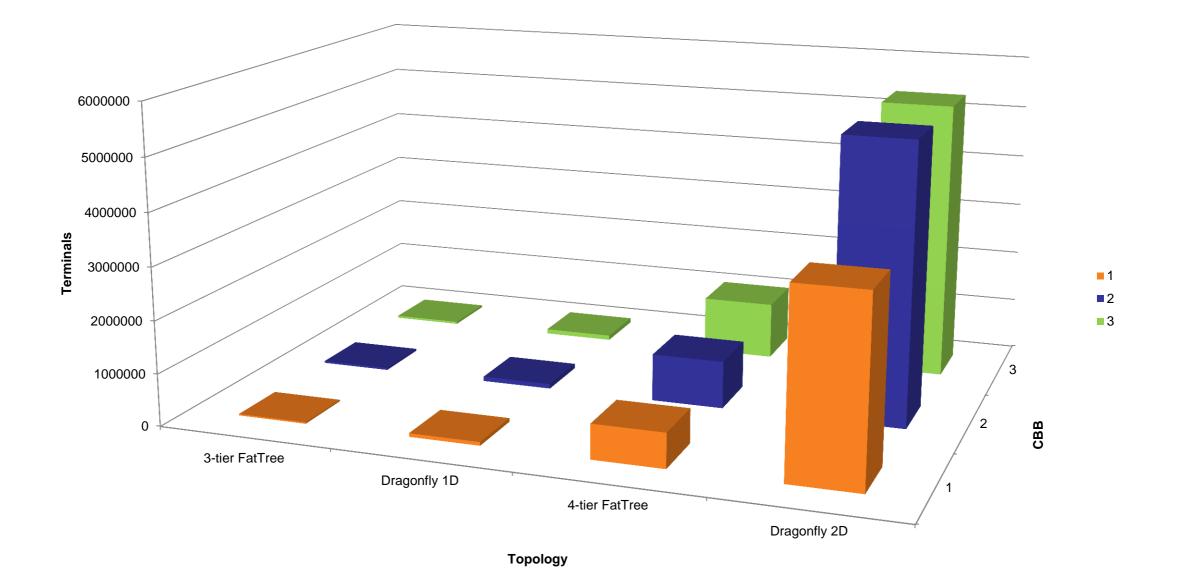
- O Two dragonflies with 1-d and 2-d flattened butterfly groups, respectively
- OA 3- and 4-level fat-tree.
- O We looked at how the blocking (CBB) and cost of these topologies develop as the size increases.
- O Load is derived from the worst case, the random permutations and the group external cases described earlier.

# SCALABILITY AND COST

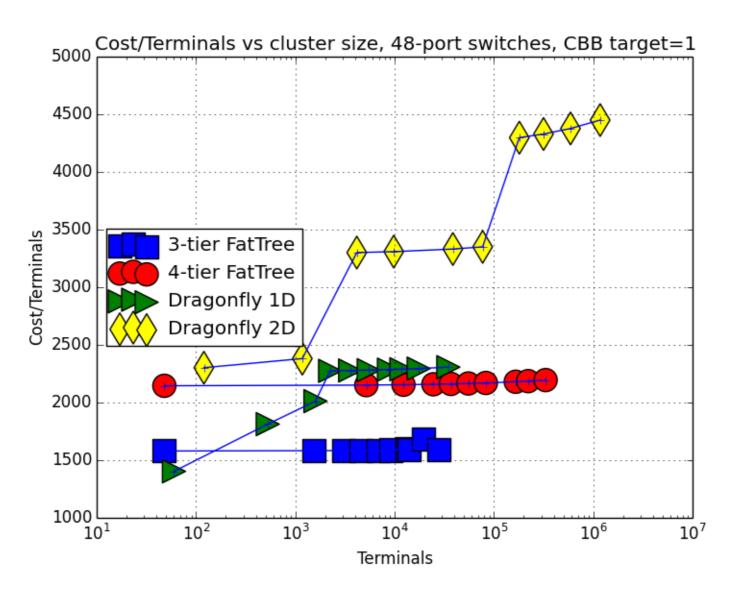
The cost of the topology is defined as:

cost = #switches × switch\_cost + #short\_links × 2m × cost\_per\_meter\_s + #long\_links × avg\_length × cost\_per\_meter\_l

#### The sizes

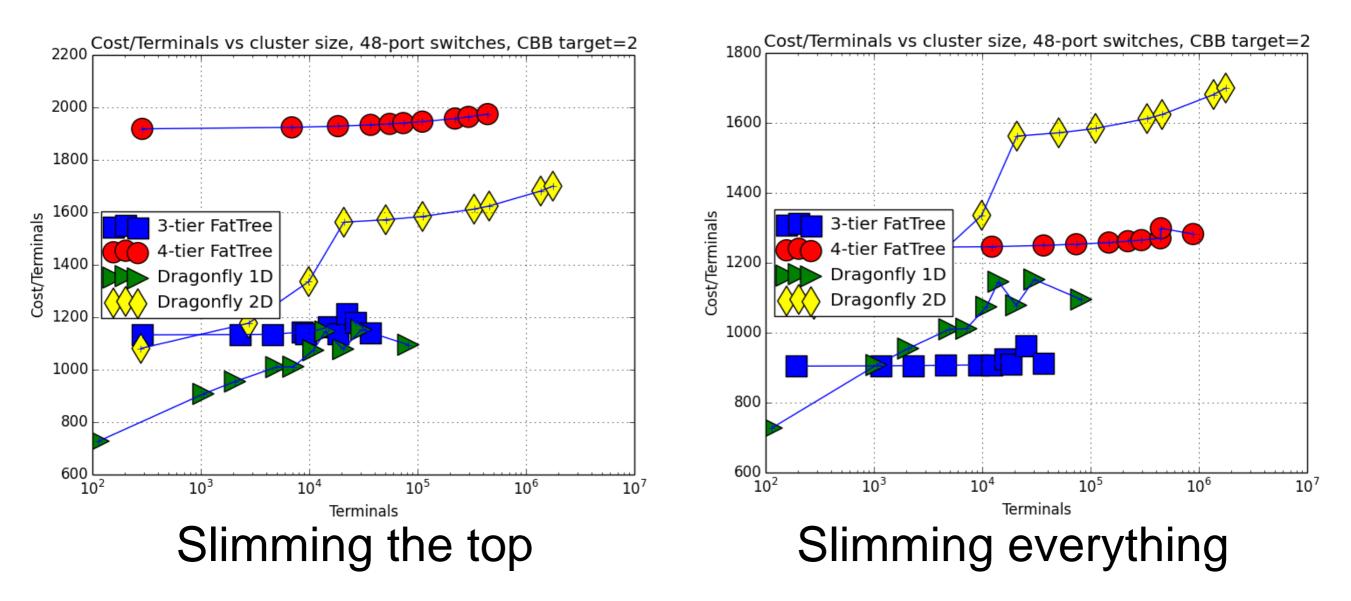


#### SCALABILITY AND COST CBB = 1



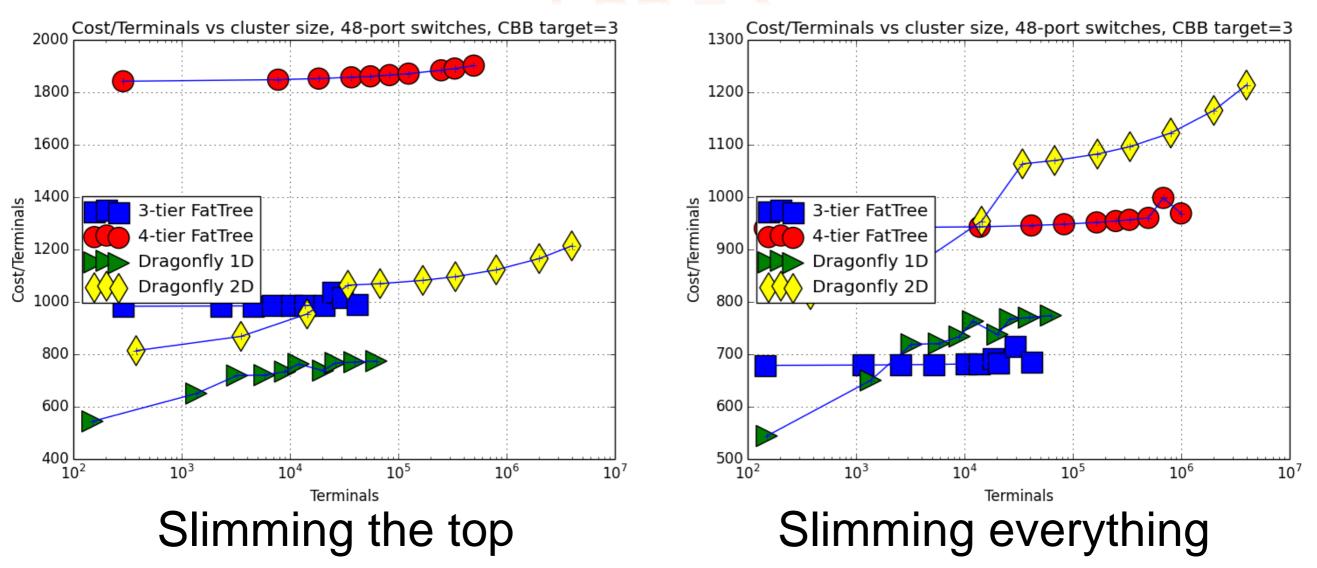
For CBB=1 the 3- and 4-tier fat-trees are more cost efficient than the dragonfly.

#### SCALABILITY AND COST CBB = 2



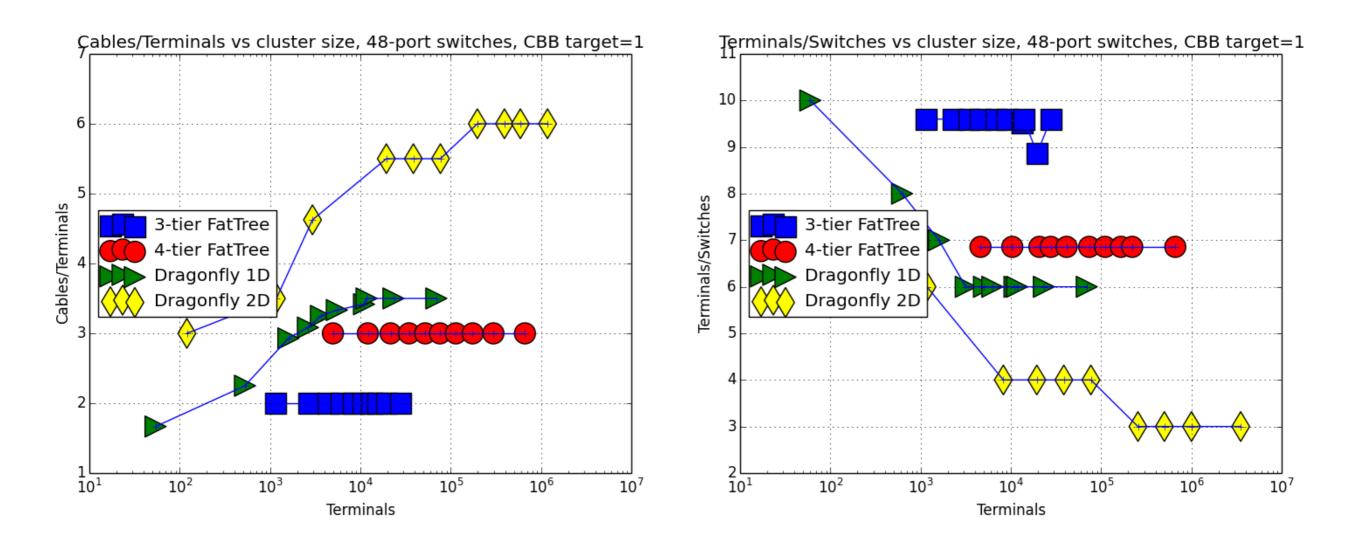
For CBB=2 the dragonfly comes into its own, but depending on how the fat tree is designed.

#### SCALABILITY AND COST CBB = 3



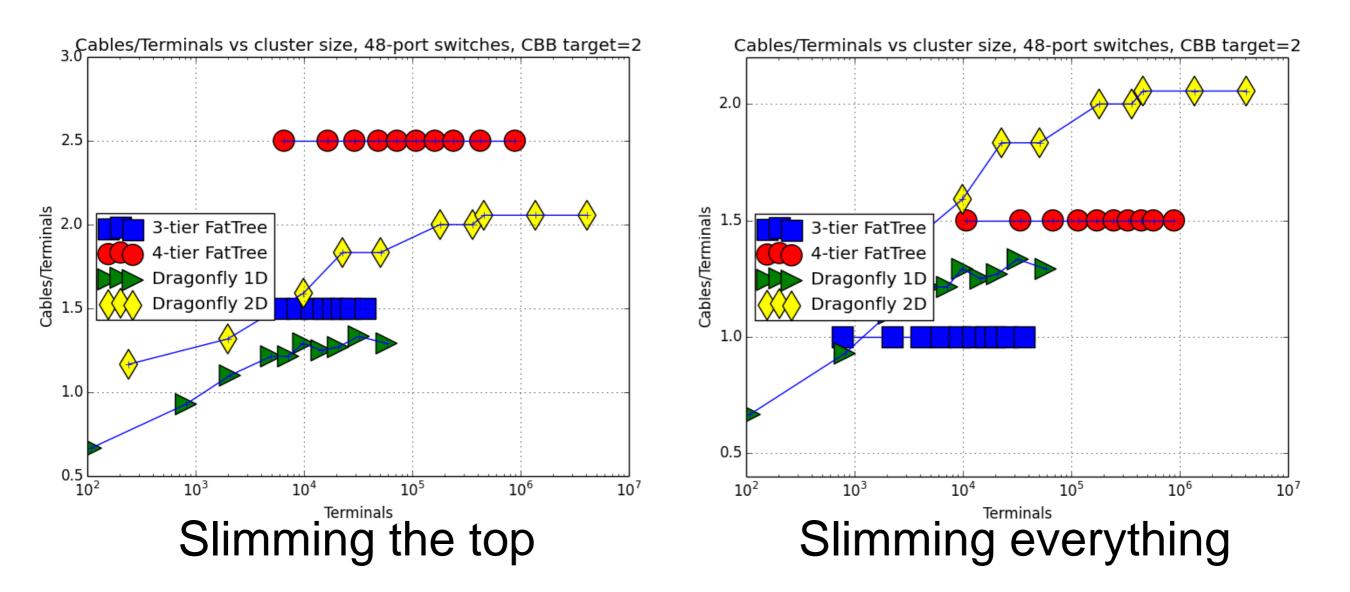
For CBB=3 the table has turned in favour of the dragonfly for any topology size, even when slimming everything.

# **STHER METRICS CBB = 1**



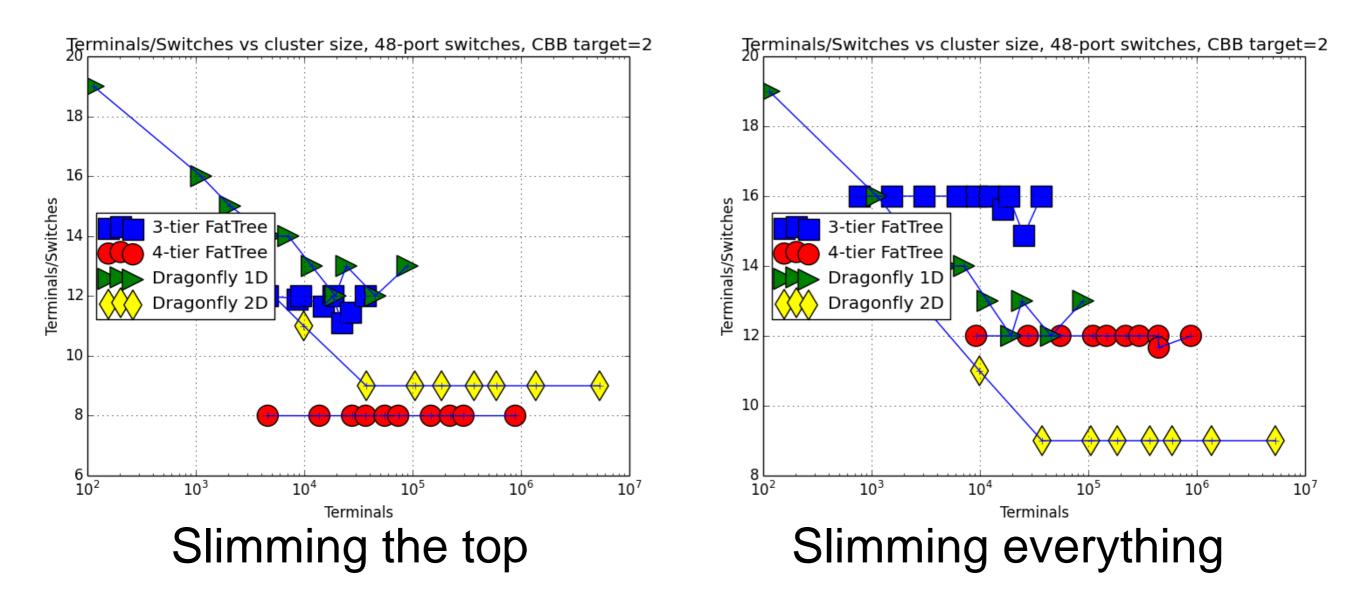
The fat trees have a much higher nonblocking efficiency in terms of cables per terminal and terminals per switch

#### **OTHER METRICS CBB = 2**



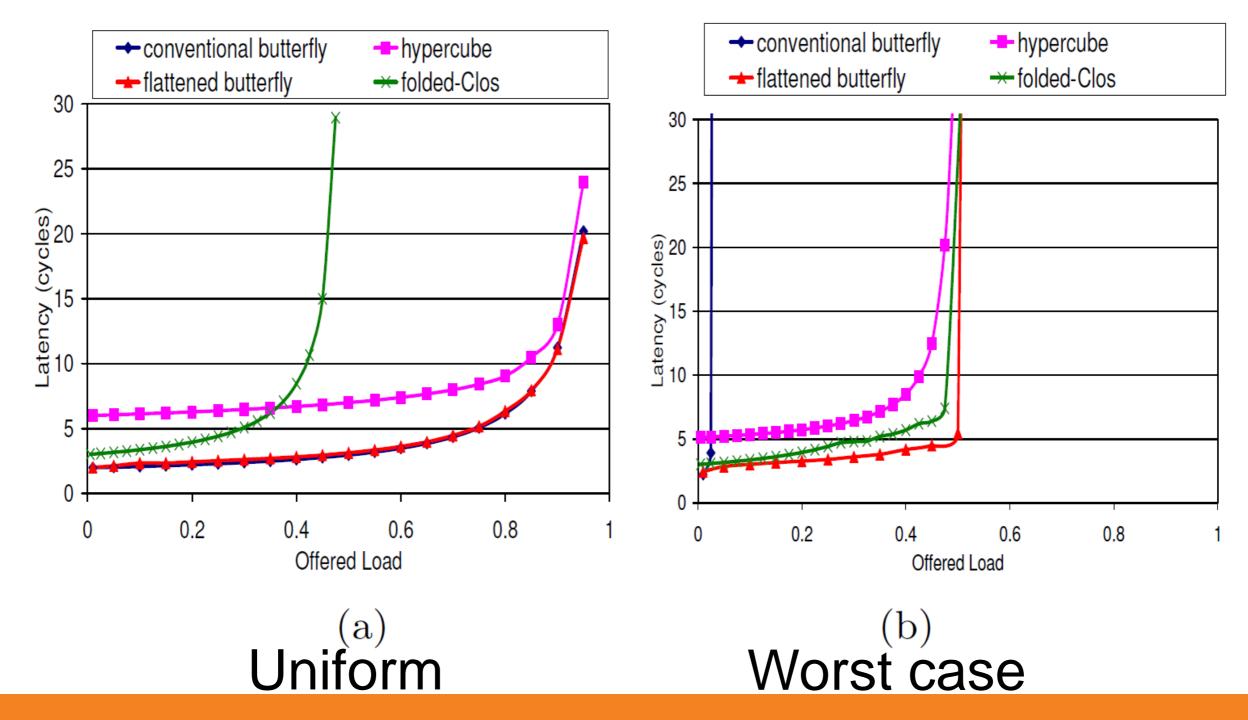
With increasing CBB ratio the cost improvement of the dragonfly over the fat tree comes to a large extent from the reduction of the number of long links

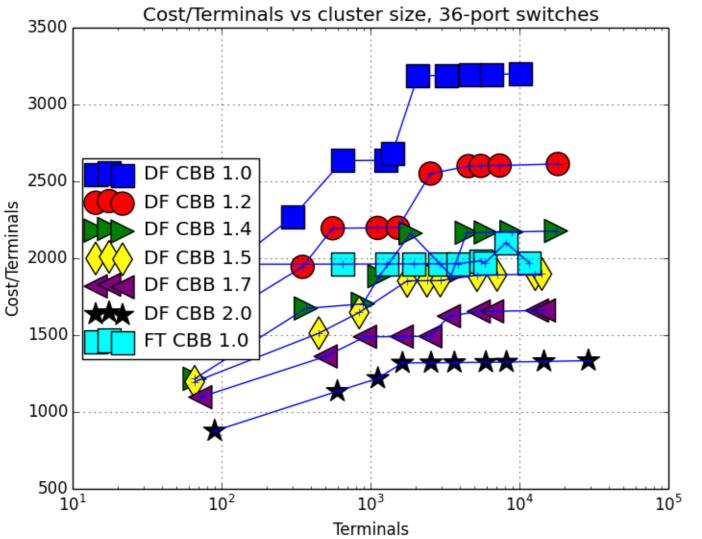
#### **STHER METRICS CBB = 2**



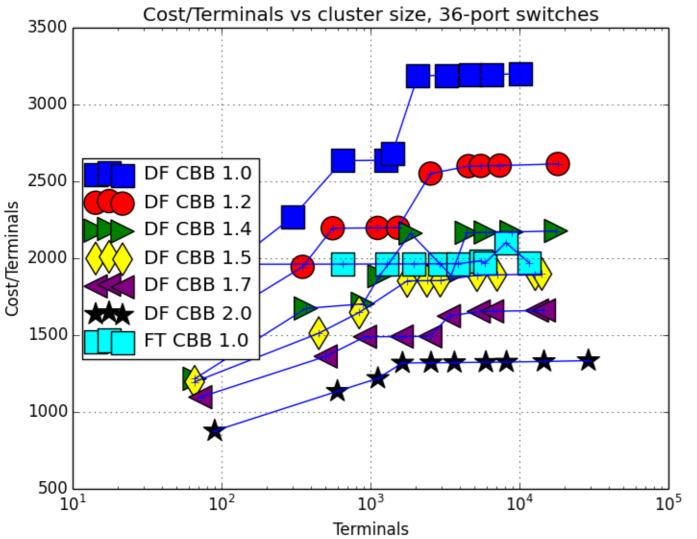
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#### Topology performance

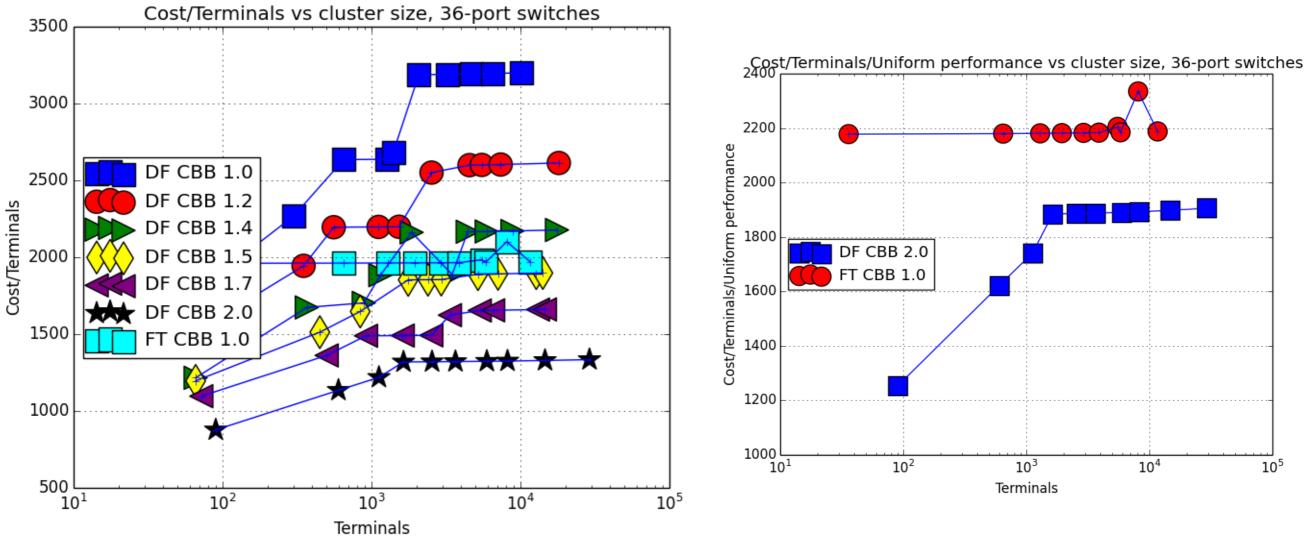




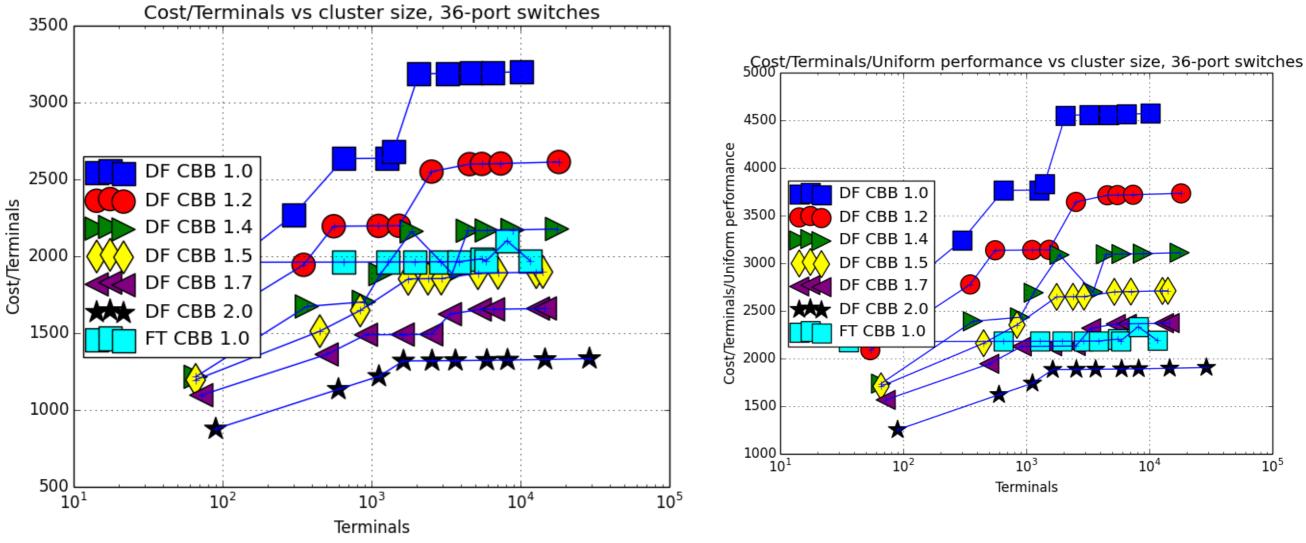
- Uniform traffic means that 50% of the traffic crosses the bisection
- Worst-case traffic means that 100% of the traffic crosses bisection
- CBB = 1.5 supports 75% of the traffic crossing the bisection



- Best practical dragonfly utilisation for uniform traffic is around 70% (adaptive routing)
  - Best practical fat tree utilisation per uniform traffic is around 90% (static routing)



Scaling with respect to performance for uniform traffic



Scaling with respect to performance for uniform traffic



Key results:

- O Comparing the dragonfly topology with different group topologies to the regular fat tree topology shows that the dragonfly is the superior choice for benign traffic patterns.
- O The dragonfly is better able to exploit higher CBB ratios to improve cost-efficiency
- The fat tree is the superior choice for more adverse traffic patterns, such as MPI collectives (at least with deterministic routing).
- O The crossing point is somewhere around 75% of the traffic crossing the bisection (or possibly lower when considering relative topology performance).

Remember:

- O The dragonfly requires support for non-minimal adaptive routing and congestion look ahead for optimal behavior, this is not supported by any existing off-the-shelf hardware, at least not with sufficient to routing performance.
- O The dragonfly requires multiple virtual channels for deadlock avoidance

# **QUESTIONS**?