Home > Data Placement for Heterogeneous Memory Systems

- Research Line Lead
  - ANTONIO PENA
    - SENIOR RESEARCHER

Computer Sciences - Accelerators and Communications for High Performance Computing

View Research Lines
- Application optimization for GPU acceleration
- Data Placement for Heterogeneous Memory Systems
- Generic Lightweight Threads
- Memory hierarchy for GPU acceleration
- Message Passing Interface (MPI)
- Neural networks for data-streams
- Preemptive multiprogramming on GPUs
- The OmpSs Programming Model

- Research Line Department / Group Leaders
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Computer Sciences

View Research Lines
- DLB: Dynamic Load Balancing
- Data Placement for Heterogeneous Memory Systems
- Runtime aware architectures
- Storage platform for data sharing
- Supercomputing for Artificial Intelligence
- The OmpSs Programming Model

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Computer Sciences - Performance Tools

View Research Lines
- Data Placement for Heterogeneous Memory Systems
- Performance Analytics

- Research Line Staff

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Computer Sciences - Performance Tools

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POSTDOCTORAL

Computer Sciences - Performance Tools

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