

[Using a Reconfigurable L1 Data Cache for Efficient Version Management in Hardware Transactional Memory](#)

Authors: [Armejach, Adrià](#) / [Seyedi, Azam](#) / [Gil, Rubén Titos](#) / [Hur, Ibrahim](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Valero, Mateo](#)

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