

## [Understanding the overhead of the spin-lock loop in CMT architectures](#)

**Authors:** [Cakarevic, Vladimir](#) / [Radojkovic, Petar](#) / [Verdú, Javier](#) / [Pajuelo, Alex](#) / [Gioiosa, Roberto](#) / [Cazorla, Francisco](#) / [Nemirovsky, Mario](#) / [Valero, Mateo](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 25 Sep 2023 - 02:47):** <https://www.bsc.es/es/research-and-development/publications/understanding-the-overhead-the-spin-lock-loop-cmt>