

Inicio > The Multi-State Processor

The Multi-State Processor

Authors: González, Isidro / Galluzzi, Marco / Cristal, Adrián / Valero, Mateo

Publication: 2007 Advanced Computer Architecture and Compilation for Embedded Systems (ACACES-07)

Place Published: L'Aquila, Italy

Pagination: 127?130

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 11 Mayo 2025 - 19:36): <u>https://www.bsc.es/es/research-and-</u>development/publications/the-multi-state-processor