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## [The limits of software transactional memory \(STM\): dissecting Haskell STM applications on a many-core environment](#)

**URL:** <http://capinfo.e.ac.upc.edu/PDFs/dir06/file003457.pdf>

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**Publication:** 5th Conference on Computing Frontiers

**Place Published:** Ischia, Italy

**Pagination:** 67?78

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

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**Source URL (retrieved on 15 Mayo 2021 - 08:11):** <https://www.bsc.es/es/research-and-development/publications/the-limits-software-transactional-memory-stm-dissecting>