

Task Superscalar: An Out-of-Order Task Pipeline

URL: <http://dx.doi.org/10.1109/MICRO.2010.13>

Authors: [Etsion, Yoav](#) / [Cabarcas, Felipe](#) / [Rico, Alejandro](#) / [Ramirez, Alex](#) / [Badia, Rosa](#) / [Ayguade, Eduard](#) / [Labarta, Jesús](#) / [Valero, Mateo](#)

Publication: IEEE/ACM Intl. Symp. on Microarchitecture (MICRO-43)

Pagination: 89-100

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 Abr 2024 - 13:47): <https://www.bsc.es/es/research-and-development/publications/task-superscalar-out-order-task-pipeline>