

## **Runtime-assisted Cache Coherence Deactivation in Task Parallel Programs**

**URL:** <http://dl.acm.org/citation.cfm?id=3291656.3291703>

**Authors:** [Caheney, Paul](#) / [Alvarez, Lluc](#) / [Valero, Mateo](#) / [Moreto, Miquel](#) / [Casas, Marc](#)

**Publication:** Proceedings of the International Conference for High Performance Computing, Networking, Storage, and Analysis (SC)

**Place Published:** Piscataway, NJ, USA

**Pagination:** 35:1?35:12

**Palabras clave:** [Cache memory](#), [memory architecture](#), [Parallel Programming](#), [runtime environment](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 24 Abr 2024 - 15:55):** <https://www.bsc.es/es/research-and-development/publications/runtime-assisted-cache-coherence-deactivation-task-parallel>