

[Profiling Transactional Memory applications on an Atomic Block Basis: A Haskell case study](#)

Authors: [Sönmez, Nehir](#) / [Cristal, Adrián](#) / [Unsal, Osman](#) / [Harris, Tim](#) / [Valero, Mateo](#)

Publication: Second Workshop on Programmability Issues for Multi-Core Computers (MULTIPROG 2009)

Place Published: Paphos, Cyprus

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 19 Abr 2024 - 20:58): <https://www.bsc.es/es/research-and-development/publications/profiling-transactional-memory-applications-atomic-block-basis>