

Predictive Runtime Code Scheduling for Heterogeneous Architectures

URL: http://dx.doi.org/10.1007/978-3-540-92990-1_4

Authors: [Jiménez, Víctor](#) / [Vilanova, Lluís](#) / [Gelado, Isaac](#) / [Gil, Marisa](#) / [Fursin, Grigori](#) / [Navarro, Nacho](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 19 Abr 2024 - 12:21): <https://www.bsc.es/es/research-and-development/publications/predictive-runtime-code-scheduling-heterogeneous-architectures>