

<u>Inicio</u> > Paving the Way Towards a Highly Energy-Efficient and Highly Integrated Compute Node for the Exascale Revolution: The ExaNoDe Approach

Paving the Way Towards a Highly Energy-Efficient and Highly Integrated Compute Node for the Exascale Revolution: The ExaNoDe Approach

URL: http://ieeexplore.ieee.org/document/8049829/

Authors: <u>Rigo</u>, Alvise / <u>Pinto</u>, <u>Christian</u> / <u>Pouget</u>, <u>Kevin</u> / <u>Raho</u>, <u>Daniel</u> / <u>Dutoit</u>, <u>Denis</u> / <u>Martinez</u>, <u>Pierre-</u> <u>Yves</u> / <u>Doran</u>, <u>Chris</u> / <u>Benini</u>, <u>Luca</u> / <u>Mavroidis</u>, <u>Iakovos</u> / <u>Marazakis</u>, <u>Manolis</u> / <u>Bartsch</u>, <u>Valeria</u> / <u>Lonsdale</u>, <u>Guy</u> / <u>Pop</u>, <u>Antoniu</u> / <u>Goodacre</u>, <u>John</u> / <u>Colliot</u>, <u>Annaik</u> / <u>Carpenter</u>, <u>Paul</u> / <u>Radojkovic</u>, <u>Petar</u> / <u>Pleiter</u>, <u>Dirk</u> / Drouin, Dominique / de Dinechin, Benoit

Publication: 2017 Euromicro Conference on Digital System Design (DSD)

Pagination: 486 - 493

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 19 Abr 2024 - 16:52): <u>https://www.bsc.es/es/research-and-</u>development/publications/paving-the-way-towards-highly-energy-efficient-and-highly-0