

[Inicio](#) > [OmpSs: A PROPOSAL FOR PROGRAMMING HETEROGENEOUS MULTI-CORE ARCHITECTURES](#)

[OmpSs: A PROPOSAL FOR PROGRAMMING HETEROGENEOUS MULTI-CORE ARCHITECTURES](#)

Authors: [Duran, Alejandro](#) / [Ayguade, Eduard](#) / [Badia, Rosa](#) / [Labarta, Jesús](#) / [Martinell, Luis](#) / [Martorell, Xavier](#) / [Planas, Judit](#)

Publication: Parallel Processing Letters

Volume / Pagination: 21 / 173-193

Palabras clave: [accelerators](#), [GPUs](#), [heterogeneous architectures](#), [OmpSs](#), [OpenCL](#), [OpenMP](#), [Parallel Programming](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 6 Dic 2023 - 15:22): <https://www.bsc.es/es/research-and-development/publications/omps-proposal-programming-heterogeneous-multi-core>