

[Morrigan: A composite instruction TLB prefetcher](#)

Authors: [Vavouliotis, Georgios](#) / [Álvarez, Lluc](#) / [Grot, Boris](#) / [Jiménez, Daniel](#) / [Casas, Marc](#)

Publication: 54th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO)

Pagination: 1138-1153

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 5 Dic 2023 - 10:29): <https://www.bsc.es/es/research-and-development/publications/morrigan-composite-instruction-tlb-prefetcher>