

## Mainstream vs. Emerging HPC: Metrics, Trade-offs and Lessons Learned

**URL:** [http://www.petarradojkovic.com/publications/SBAC-PAD-2018\\_Radulovic.pdf](http://www.petarradojkovic.com/publications/SBAC-PAD-2018_Radulovic.pdf)

**Authors:** [Radulovic, Milan](#) / [Asifuzzaman, Kazi](#) / [Zivanovic, Darko](#) / [Rajovic, Nikola](#) / [de Verdière, Guillaume](#) / [Pleiter, Dirk](#) / [Marazakis, Manolis](#) / [Kallimanis, Nikolaos](#) / [Carpenter, Paul](#) / [Radojkovic, Petar](#) / [Ayguadé, Eduard](#)

**Research Lines:** [Memory systems for HPC](#) / [Microserver architectures and system software](#)

**Publication:** 30th International Symposium on Computer Architecture and High Performance Computing (SBAC-PAD 2018)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 20 Mar 2023 - 14:06):** <https://www.bsc.es/es/research-and-development/publications/mainstream-vs-emerging-hpc-metrics-trade-offs-and-lessons>