

[Integrating Dataflow Abstractions into Transactional Memory](#)

Authors: [Gajinov, Vladimir](#) / [Milovanovic, Milos](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Ayguade, Eduard](#) / [Valero, Mateo](#)

Publication: First Workshop on Systems for Future Multi-Core Architectures (SFMA'11)

Place Published: Salzburg, Austria

Pagination: 1?6

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 Jun 2024 - 17:35): <https://www.bsc.es/es/research-and-development/publications/integrating-dataflow-abstractions-transactional-memory>