

[Increasing Multicore System Efficiency through Intelligent Bandwidth Shifting](#)

URL: <http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=7056020&isnumber=7056013>

Authors: [Jiménez, Víctor](#) / [Buyuktosunoglu, Alper](#) / [Bose, Pradip](#) / [O'Connell, Franck](#) / [Cazorla, Francisco](#) / [Valero, Mateo](#)

Publication: International Symposium on High- Performance Computer Architecture (HPCA)

Pagination: 39-50

Palabras clave: [IBMBSC](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 18 Sep 2021 - 07:17): <https://www.bsc.es/es/research-and-development/publications/increasing-multicore-system-efficiency-through-intelligent>