

Published on BSC-CNS (<https://www.bsc.es>)

[Inicio](#) > Hardware Acceleration for Query Processing: Leveraging FPGAs, CPUs, and Memory

Hardware Acceleration for Query Processing: Leveraging FPGAs, CPUs, and Memory

URL: <http://ieeexplore.ieee.org/document/7361655/>

Authors: [Arcas-Abella, Oriol](#) / [Armejach, Adrià](#) / [Hayes, Timothy](#) / [Malazgirt, Gorker](#) / [Palomar, Oscar](#) / [Salami, Behzad](#) / [Sonmez, Nehir](#)

Publication: Computing in Science & Engineering

Volume / Pagination: 18 / 80 - 87

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 Abr 2024 - 12:47): <https://www.bsc.es/es/research-and-development/publications/hardware-acceleration-query-processing-leveraging-fpgas-cpus>