

A Flexible Hybrid Transactional Memory Multicore on FPGA

Authors: [Arcas, Oriol](#) / [Sönmez, Nehir](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Valero, Mateo](#)

Publication: Jornadas de Paralelismo 2011

Place Published: La Laguna, Tenerife, Spain

Pagination: 283?289

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 16 Jun 2024 - 06:00): <https://www.bsc.es/es/research-and-development/publications/flexible-hybrid-transactional-memory-multicore-fpga>