

[DMA++: On the Fly Data Realignment for On-Chip Memories](#)

Authors: [Vujic](#), / [Cabarcas, Felipe](#) / [González, Marc](#) / [Ramirez, Alex](#) / [Martorell, Xavier](#) / [Ayguade, Eduard](#)

Publication: Computers, IEEE Transactions on

Volume / Number / Pagination: 61 / 2 / 237 -250

Palabras clave: [compiler generated code](#), [data realignment](#), [DMA transfers](#), [DMA++](#), [integrated memory circuits](#), [memory alignment constraints](#), [multimedia computing](#), [multimedia extensions](#), [on-chip memories](#), [parallel processing](#), [program compilers](#), [single instruction multiple data units](#), [store instructions](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 19 Sep 2024 - 06:26): <https://www.bsc.es/es/research-and-development/publications/dma-the-fly-data-realignment-chip-memories-0>