

A Distributed Processor State Management Architecture for Large-Window Processors

Authors: [González, Isidro](#) / [Galluzzi, Marco](#) / [Veidenbaum, Alexander](#) / [Ramírez, Marco](#) / [Cristal, Adrián](#) / [Valero, Mateo](#)

Number: UPC-DAC-RR-CAP-2008-10

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 23 Abr 2024 - 10:33): <https://www.bsc.es/es/research-and-development/publications/distributed-processor-state-management-architecture-large-0>