

<u>Inicio</u> > Analyzing the effect of last level cache sharing on integrated platforms with fine-grain CPU-GPU collaboration

Analyzing the effect of last level cache sharing on integrated platforms with fine-grain CPU-GPU collaboration

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Research Lines: Memory hierarchy for GPU acceleration

Publication: GPU Technology Conference Euope (GTC Europe)

Place Published: Amsterdam, The Netherlands

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 19 Abr 2024 - 12:06): <u>https://www.bsc.es/es/research-and-</u>development/publications/analyzing-the-effect-last-level-cache-sharing-integrated