



[Inicio](#) > Analysis and RTL Correlation of Instruction Set Simulators for Automotive Microcontroller Robustness Verification

Analysis and RTL Correlation of Instruction Set Simulators for Automotive Microcontroller Robustness Verification

URL:

http://ieeexplore.ieee.org/xpl/articleDetails.jsp?tp=&arnumber=7167224&openedRefinements%3D*%26filter%3DA

Authors: [Espinosa, Jaime](#) / [Hernández, Carles](#) / [Abella, Jaume](#) / [De Andres, David](#) / [Ruiz, Juan](#)

Publication: Design Automation Conference (DAC), 2015 52nd ACM/EDAC/IEEE

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 8 Mayo 2024 - 21:52): <https://www.bsc.es/es/research-and-development/publications/analysis-and-rtl-correlation-instruction-set-simulators>