

5th Int. Conf. on High Performance Embedded Architectures and Compilers (HiPEAC 2010)

URL: <http://www.informatik.uni-trier.de/ley/db/conf/hipeac/hipeac2010.html>

Authors: [Patt, Yale](#) / [Foglia, Pierfrancesco](#) / [Duesterwald, Evelyn](#) / [Faraboschi, Paolo](#) / [Martorell, Xavier](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 3 Dic 2023 - 09:32): <https://www.bsc.es/es/research-and-development/publications/5th-int-conf-high-performance-embedded-architectures-and>