Published on *BSC-CNS* (https://www.bsc.es)

Power Estimation Tool for System on Programmable Chip based Platforms

Authors: Rethinagiri, Santhosh / Palomar, Oscar / Cristal, Adrián / Unsal, Osman

Teams: Computer Architecture For Parallel Paradigms / Resilience In Distributed Systems

Publication: 22nd ACM/SIGDA International Symposium on Field-Programmable Gate Arrays

Place Published: Monterey, CA, United States

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on *29 Mar 2024 - 13:33*): https://www.bsc.es/es/node/40887