

## **FaultTM: Error Detection and Recovery Using Hardware Transactional Memory**

**Authors:** [Yalcin, Gulay](#) / [Unsal, Osman](#) / [Cristal, Adrián](#)

**Publication:** ACM/IEEE Design, Automation, and Test in Europe (DATE)

**Place Published:** Grenoble, France

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 5 Mar 2024 - 13:03):** <https://www.bsc.es/es/node/40582>