

[VELOX Project launches first fully integrated Transactional Memory Stack](#)



It seems that these days everyone wants a “supercomputer” in their pocket. Consumers are demanding more performance and power from smaller and sleeker devices. However, even the latest gadgets have physical size limitations that will soon impede their ability to support consumer requirements, which could translate into a future rife with slower and clunkier devices. The performance wall represents a serious issue that vendors are addressing via the multi-core processors that are now showing up in everyday devices from game consoles to cell phones. But, they haven’t addressed the problem entirely. Writing software for these multi-core processors, also known as parallel programming is difficult and requires specialized training and experience. Transactional Memory is a new technology that greatly simplifies parallel programming.

[Press release](#)

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