

Mont-Blanc: European approach towards energy efficient HPC

Energy efficiency is already a primary concern for the design of any computer system and it is unanimously recognized that future Exascale systems will be strongly constrained by their power consumption. This two minutes long video gives a quick overview about the Mont-Blanc project, whose aim is to design a new type of computer architecture capable of setting future global HPC standards, built from today's energy efficient solutions used in embedded and mobile devices.

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