

Validating a Timing Simulator for the NGMP Multicore Processor

Authors: [Jalle, Javier](#) / [Abella, Jaume](#) / [Fossati, Luca](#) / [Zulianello, Marco](#) / [Cazorla, Francisco](#)

Publication: 21st Data Systems In Aerospace Conference (DASIA)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 abr 2024 - 15:46): <https://www.bsc.es/ca/research-and-development/publications/validating-timing-simulator-the-ngmp-multicore-processor>