

Use of Randomized Caches in Hard Real-Time Systems

Authors: [Kosmidis, Leonidas](#) / [Quinones, Eduardo](#) / [Abella, Jaume](#) / [Cazorla, Francisco](#) / [Bernat, Guillem](#) / [Berger, Emery](#)

Research Lines: [Probabilistic Real Time Systems](#)

Publication: 6th ACACES 2010 (Advanced Computer Architecture and Compilation for High-Performance and Embedded Systems)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 20 abr 2024 - 04:15): <https://www.bsc.es/ca/research-and-development/publications/use-randomized-caches-hard-real-time-systems>