

Techniques to Improve Concurrency in Hardware Transactional Memory

Authors: [Armejach, Adrià](#)

Thesis Supervisor: Adrian Cristal and Osman Unsal

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 18 abr 2024 - 10:52): <https://www.bsc.es/ca/research-and-development/publications/techniques-improve-concurrency-hardware-transactional-memory>