

## STT-MRAM for real-time embedded systems: performance and WCET implications

**URL:** <http://dl.acm.org/citation.cfm?doid=3357526>

**UPCommons Handle URL** <http://hdl.handle.net/2117/187922>

**Authors:** [Asifuzzaman, Kazi](#) / [Fernandez, Mikel](#) / [Radojkovic, Petar](#) / [Abella, Jaume](#) / [Cazorla, Francisco](#)

**Publication:** Proceedings of the International Symposium on Memory Systems - MEMSYS '19

**Pagination:** 195 - 205

**Paraules clau:** [computer systems organization](#), [Computing methodologies](#), [Non-volatile memory Embedded systems](#), [Processors and memory architectures](#), [Real-time simulation](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 20 Mar 2023 - 20:56):** <https://www.bsc.es/ca/research-and-development/publications/stt-mram-real-time-embedded-systems-1>