

Software Caching Techniques and Hardware Optimizations for On-Chip Local Memories

Authors: [Vujic, Nikola](#)

Thesis Supervisor: Mac Gonzalez Tallada and Eduard Ayguade

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 7 mai 2024 - 00:06): <https://www.bsc.es/ca/research-and-development/publications/software-caching-techniques-and-hardware-optimizations-chip>