

## [Runtime-Guided Management of Stacked DRAM Memories in Task Parallel Programs](#)

**URL:** <http://dl.acm.org/citation.cfm?doid=3205289>

**Authors:** [Alvarez, Lluc](#) / [Casas, Marc](#) / [Labarta, Jesus](#) / [Ayguadé, Eduard](#) / [Valero, Mateo](#) / [Moreto, Miquel](#)

**Publication:** Proceedings of the 2018 International Conference on Supercomputing - ICS '18

**Pagination:** 218 - 228

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 18 jul 2024 - 20:08):** <https://www.bsc.es/ca/research-and-development/publications/runtime-guided-management-stacked-dram-memories-task-parallel>