

Inici > Racing to Hardware-Validated Simulation

Racing to Hardware-Validated Simulation

URL: https://ieeexplore.ieee.org/document/8695650/

Authors: Adileh, Almutaz / González-Álvarez, Cecilia / Ruiz, Juan / Eeckhout, Lieven

Publication: 2019 IEEE International Symposium on Performance Analysis of Systems and Software

(ISPASS)

Pagination: 58 - 67

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (**retrieved on 24 abr 2024 - 17:56**): https://www.bsc.es/ca/research-and-development/publications/racing-hardware-validated-simulation