

## PPMC : A Programmable Pattern based Memory Controller

**Authors:** [Hussain, Tassadaq](#) / [Shafiq, Muhammad](#) / [Pericas, Miquel](#) / [Nacho, Navarro](#) / [Ayguade, Eduard](#)

**Publication:** Applied Reconfigurable Computing (ARC)

**Place Published:** HongKong, China

**Pagination:** 1?12

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 27 set 2023 - 23:48):** <https://www.bsc.es/ca/research-and-development/publications/ppmc-programmable-pattern-based-memory-controller>