

Performance Impact of a Slower Main Memory: A case study of STT-MRAM in HPC

URL: <http://dl.acm.org/citation.cfm?id=2989082>

UPCommons Handle URL <http://upcommons.upc.edu/handle/2117/91091>

Authors: [Asifuzzaman, Kazi](#) / [Pavlovic, Milan](#) / [Radulovic, Milan](#) / [Zaragoza, David](#) / [Kwon, Ohseong](#) / [Ryoo, Kyung-Chang](#) / [Radojkovic, Petar](#)

Research Lines: [Memory systems for HPC](#)

Publication: Proceedings of the Second International Symposium on Memory Systems (MEMSYS)

Place Published: ICPS: ACM International Conference Proceeding Series

Pagination: 40-49

Paraules clau: [HPC](#), [Main Memory](#), [STT-MRAM](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 7 des 2023 - 18:39): <https://www.bsc.es/ca/research-and-development/publications/performance-impact-slower-main-memory-case-study-stt-mram-hpc>