

Inici > MVPA: An FPGA based multi-vector processor architecture

MVPA: An FPGA based multi-vector processor architecture

URL: http://ieeexplore.ieee.org/document/7429879/

Authors: Hussain, Tassadaq / Palomar, Oscar / Cristal, Adrian / Ayguade, Eduard / Haider, Amna

Publication: 2016 13th International Bhurban Conference on Applied Sciences and Technology (IBCAST)

Place Published: Islamabad, Pakistan

Pagination: 213 - 218

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (**retrieved on** *24 abr 2024 - 13:16*): https://www.bsc.es/ca/research-and-development/publications/mvpa-fpga-based-multi-vector-processor-architecture