

## [Multiple Target Task Sharing Support for the OpenMP Accelerator Model](#)

**URL:** [http://link.springer.com/chapter/10.1007%2F978-3-319-45550-1\\_19](http://link.springer.com/chapter/10.1007%2F978-3-319-45550-1_19)

**UPCommons Handle URL** <http://upcommons.upc.edu/handle/2117/91300>

**Authors:** [Ozen, Guray](#) / [Mateo, Sergi](#) / [Ayguade, Eduard](#) / [Labarta, Jesús](#) / [Beyer, James](#)

**Publication:** OpenMP: Memory, Devices, and Tasks. 12th International Workshop on OpenMP, IWOMP 2016, Nara, Japan, October 5-7, 2016, Proceedings

**Volume / Pagination:** Lecture Notes in Computer Science, 9903 / 268 - 280

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 20 jun 2024 - 14:20):** <https://www.bsc.es/ca/research-and-development/publications/multiple-target-task-sharing-support-the-openmp-accelerator>