

A Multicore Emulator with a Profiling Infrastructure for Transactional Memory on FPGA

Authors: [Sonmez, Nehir](#)

Thesis Supervisor: Osman Unsal and Adrian Cristal

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 abr 2024 - 18:40): <https://www.bsc.es/ca/research-and-development/publications/multicore-emulator-profiling-infrastructure-transactional>