

Implicit Transactional Memory in Kilo-Instruction Multiprocessor

Authors: [Galluzzi, Marco](#) / [Vallejo, Enrique](#) / [Cristal, Adrián](#) / [Vallejo, Fernando](#) / [Beivide, Ramón](#) / [Stenström, Per](#) / [Smith, James](#) / [Valero, Mateo](#)

Publication: The Twelfth Asia-Pacific Computer Systems Architecture Conference (ACSAC 2007)

Place Published: Seoul, South Korea

Pagination: 339?353

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 17 abr 2024 - 15:45): <https://www.bsc.es/ca/research-and-development/publications/implicit-transactional-memory-kilo-instruction-multiprocessor>