

## **GRID superscalar: from the Grid to the Cell processor**

**Authors:** [Badia, Rosa](#) / [Bellens, Pieter](#) / [de Palol, Marc](#) / [Ejarque, Jorge](#) / [Labarta, Jesús](#) / [Pérez, Josep](#) / [Sirvent, Raul](#) / [Tejedor, Enric](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 1 des 2023 - 07:32):** <https://www.bsc.es/ca/research-and-development/publications/grid-superscalar-the-grid-the-cell-processor>