

Fitting processor architectures for measurement-based probabilistic timing analysis

URL: <http://www.sciencedirect.com/science/article/pii/S0141933116300977>

Authors: [Kosmidis, Leonidas](#) / [Quinones, Eduardo](#) / [Abella, Jaume](#) / [Vardanega, Tullio](#) / [Hernández, Carles](#) / [Gianarro, Andrea](#) / [Broster, Ian](#) / [Cazorla, Francisco](#)

Publication: Microprocessors and Microsystems

Volume / Pagination: 47 / 287 - 302

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 19 abr 2024 - 04:07): <https://www.bsc.es/ca/research-and-development/publications/fitting-processor-architectures-measurement-based>