Chai: collaborative heterogeneous applications for integrated-architectures

URL: http://impact.crhc.illinois.edu/shared/Papers/chai-ispass17.pdf

Authors: Gomez-Luna, Juan / Hajj, Izzat / Chang, Li-Wen / Garcia, Victor / de Gonzalo, Simon / Jablin, Thomas / Pena, Antonio / Hwu, Wen-mei

Research Lines: Memory hierarchy for GPU acceleration

Publication: IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS)

Place Published: Santa Rosa, California, USA

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on *26 abr 2024 - 14:49*): https://www.bsc.es/ca/research-and-development/publications/chai-collaborative-heterogeneous-applications-integrated