

CellSim: A Cell Processor Simulation Infrastructure

Authors: [Cabarcas, Felipe](#) / [Rico, Alejandro](#) / [Ródenas, David](#) / [Martorell, Xavier](#) / [Ramirez, Alex](#) / [Ayguade, Eduard](#)

Publication: 2007 Advanced Computer Architecture and Compilation for Embedded Systems (ACACES-07)

Pagination: 279-282

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 28 mai 2023 - 11:45): <https://www.bsc.es/ca/research-and-development/publications/cellsim-cell-processor-simulation-infrastructure>