

[Boosting irregular array Reductions through In-lined Block-ordering on fast processors](#)

URL: <http://ieeexplore.ieee.org/xpl/articleDetails.jsp?arnumber=7322443>

Authors: [Ciesko, Jan](#) / [Mateo, S.](#) / [Teruel, X.](#) / [Beltran, Vicenç](#) / [Martorell, Xavier](#) / [Labarta, Jesús](#)

Publication: High Performance Extreme Computing Conference (HPEC), 2015 IEEE

Pagination: 1-6

Paraules clau: [array reduction](#), [Arrays](#), [Bandwidth](#), [data reduction](#), [Instruction sets](#), [multicore system](#), [multiprocessing systems](#), [OpenMP](#), [Parallel Programming](#), [parallel programming model](#), [PIBOR](#), [Privatization](#), [privatization with in-lined block-ordered reduction](#), [processor cycle](#), [Resource management](#), [Runtime](#), [runtime support](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 set 2023 - 09:41): <https://www.bsc.es/ca/research-and-development/publications/boosting-irregular-array-reductions-through-lined-block>