

## [Automatic frequency scaling for embedded coprocessor acceleration](#)

**URL:**

[https://www.hipeac.net/media/private/events/subactivity/a\\_pena\\_\\_opera\\_hipeac\\_automatic\\_freq\\_scaling\\_for\\_embed](https://www.hipeac.net/media/private/events/subactivity/a_pena__opera_hipeac_automatic_freq_scaling_for_embed)

**Authors:** [Pena, Antonio](#) / [Mantovani, Filippo](#)

**Research Lines:** [Application optimization for GPU acceleration](#)

**Publication:** Workshop on Heterogeneous and Low-Power Data Center technologies (HeLP-DC)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 26 set 2024 - 23:24):** <https://www.bsc.es/ca/research-and-development/publications/automatic-frequency-scaling-embedded-coprocessor-acceleration>