

## [Assessing Accelerator-based HPC Reverse Time Migration](#)

**Authors:** [Araya-Polo, Mauricio](#) / [Cabezas, Javier](#) / [Hanzich, Mauricio](#) / [Pericas, Miquel](#) / [Morancho, Enric](#) / [Gelado, Isaac](#) / [Shafiq, Muhhamad](#) / [Rubio, Felix](#) / [Cela, José Ma.](#) / [Ayguade, Eduard](#) / [Navarro, Nacho](#) / [Valero, Mateo](#)

**Publication:** Transactions on Parallel and Distributed Systems, Special Issue on Accelerators

**Volume / Pagination:** 22(1) / 147-162

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 23 set 2020 - 19:42):** <https://www.bsc.es/ca/research-and-development/publications/assessing-accelerator-based-hpc-reverse-time-migration>