

Architectural Support for Real-Time Task Scheduling in SMT Processors

Authors: [Cazorla, Francisco](#) / [Knijnenburg, Peter](#) / [Sakellariou, Rizos](#) / [Fernández, Enrique](#) / [Ramirez, Alex](#) / [Valero, Mateo](#)

Publication: International Conference on Compilers, Architecture, and Synthesis for Embedded Systems (CASES-2005)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 24 abr 2024 - 14:27): <https://www.bsc.es/ca/research-and-development/publications/architectural-support-real-time-task-scheduling-smt-processors>