

Published on BSC-CNS (https://www.bsc.es)

Inici > Achieving High Memory Performance from Heterogeneous Architectures with the SARC **Programming Model**

Achieving High Memory Performance from Heterogeneous Architectures with the SARC Programming Model

URL: http://portal.acm.org/citation.cfm?id=1621963

Authors: Ferrer, Roger / Beltran, Vicenç / González, Marc / Martorell, Xavier / Ayguade, Eduard

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 19 abr 2024 - 05:05): https://www.bsc.es/ca/research-anddevelopment/publications/achieving-high-memory-performance-heterogeneous-architectures