

## [Timing Verification of Fault-Tolerant Chips for Safety-Critical Applications in Harsh Environments](#)

**Authors:** [Slijepcevic, Mladen](#) / [Kosmidis, Leonidas](#) / [Abella, Jaume](#) / [Quinones, Eduardo](#) / [Cazorla, Francisco](#)

**Publication:** IEEE Micro

**Volume / Number / Pagination:** 34 / 6 / 8?19

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 26 jul 2021 - 18:26):** <https://www.bsc.es/ca/node/40920>